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\*\* CONTINUING DATA \*\*\*\*\*

NONE ☒

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

NONE ☒

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CT	SHEETS DRAWING 3	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		
Verified and Acknowledged				

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## TITLE

Method and apparatus to extend the number of instruction bits in processors with fixed length instructions, in a manner compatible with existing code

☐ All Fees☐ 1.16 Fees ( Filing )